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JUL 07 2003

Inventor(s): Zhizhang Chen et al

Confirmation No.: 9286

Application No.: 09/817703

Examiner: Foong, Suk San

Filing Date: Mar 26, 2001

Group Art Unit: 2823X

Title: LDMOS And CMOS Integrated Circuit And Method Of Making

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

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TECHNOLOGY CENTER 2823
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TRANSMITTAL LETTER FOR RESPONSE/AMENDMENT

Sir:

Transmitted herewith is/are the following in the above-identified application:

- (X) Response/Amendment () Petition to extend time to respond
() New fee as calculated below () Supplemental Declaration
(X) No additional fee (Address envelope to "Mail Stop Non-Fee Amendment")
() Other: _____ (fee \$ _____)

CLAIMS AS AMENDED BY OTHER THAN A SMALL ENTITY						
(1) FOR	(2) CLAIMS REMAINING AFTER AMENDMENT	(3) NUMBER EXTRA	(4) HIGHEST NUMBER PREVIOUSLY PAID FOR	(5) PRESENT EXTRA	(6) RATE	(7) ADDITIONAL FEES
TOTAL CLAIMS	4	MINUS	20	= 0	X \$18	\$ 0
INDEP. CLAIMS	4	MINUS	4	= 0	X \$84	\$ 0
[] FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM					+ \$280	\$ 0
EXTENSION FEE	1ST MONTH \$110.00	2ND MONTH \$410.00	3RD MONTH \$930.00	4TH MONTH \$1450.00		\$ 0
OTHER FEES						\$
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT						\$ 0

Charge \$ 0 to Deposit Account 08-2025. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account 08-2025 pursuant to 37 CFR 1.25. Additionally please charge any fees to Deposit Account 08-2025 under 37 CFR 1.16 through 1.21 inclusive, and any other sections in Title 37 of the Code of Federal Regulations that may regulate fees. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

Zhizhang Chen et al

By



Timothy F. Myers

Attorney/Agent for Applicant(s)
Reg. No. 42,919

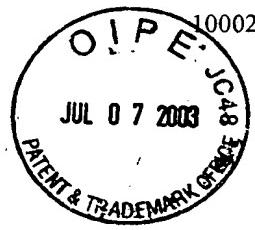
Date: 7/2/03

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450.

Date of Deposit: 7/2/03

Typed Name: Timothy F. Myers

Signature: 



AMENDMENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

5 In re application of: Z. Chen et al.

Art Unit: 2823

Examiner: Foong, Suk San

Serial Number: 09/817,703

Filed: March 26, 2001

Title: LDMOS AND CMOS INTEGRATED CIRCUIT AND METHOD

10 OF MAKING

Date: July 2, 2003

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AMENDMENT UNDER 37 CFR § 1.116

15 TO THE ASSISTANT COMMISSIONER FOR PATENTS:

Sir:

In response to the Final Office Action dated May 2, 2003, please amend the above-identified patent application as follows: